

Transconductance Amplifier Buffers Current Transformer

Three forms of transconductance amplifier are presented, with emphasis on the configuration suitable for use with current transformers.

Current transformers, which offer a convenient means for current measurement in many applications, also provide isolation while absorbing very little power from the source. The output current from a current transformer can be converted to voltage with a resistor of a suitable value, but that approach can dramatically reduce the transformer's high frequency response.

Transconductance amplifiers provide a good method for converting current to voltage in most current-measurement applications. The current source feeds into the virtual ground of an op amp, and the transconductance can be adjusted by changing the value of a single resistor (Figure 1). As another benefit of this arrangement, the circuit gain for input offset voltage is no greater than unity.

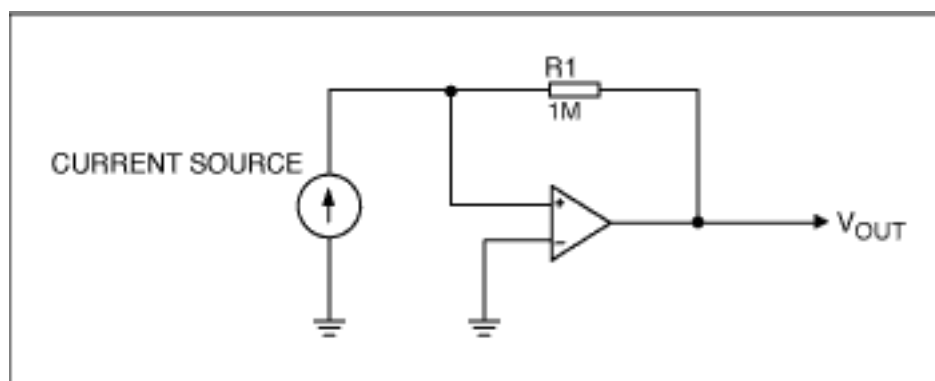


Figure 1. For resistor values less than $20M\Omega$, this simple transconductance amplifier is acceptable for use with some current sources.

Resistor values above $20M\Omega$ are notoriously difficult to apply. For applications requiring a transconductance above $20M\Omega$, consider the circuit of Figure 2. The output voltage divider adds voltage gain that multiplies the transconductance value, enabling effective transconductances greater than $200M\Omega$. Note that the gain for input offset voltage now equals the additional voltage gain.

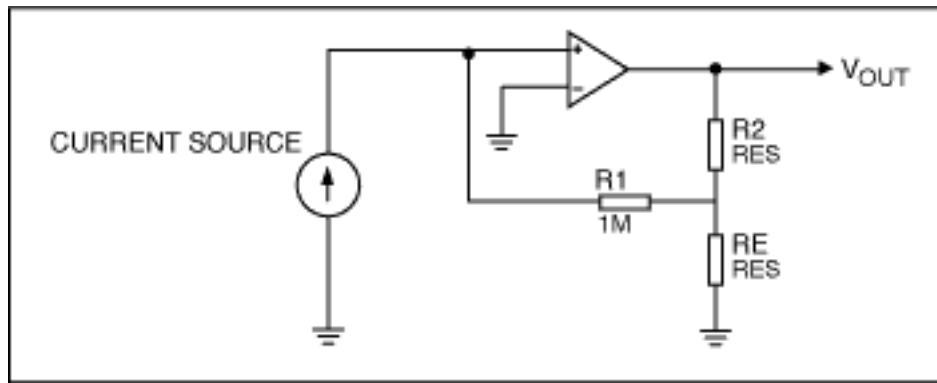


Figure 2. Additional voltage gain in this circuit (vs. that of Figure 1) provides effective transconductances greater than $200\text{M}\Omega$.

These forms of the transconductance amplifier are useful for inputs that closely resemble an ideal current source, as does (for example) a photo-diode preamplifier, but they are not suitable for use with a current transformer. A current transformer is similar to an ideal current source, but its source impedance goes to zero at dc. (True current sources exhibit infinite impedance at all frequencies.)

The virtual ground (summing node) at the op amp's inverting input is not at zero potential, but at a potential equal to the op amp's input offset voltage. Thus, connecting a current transformer to the input shorts that summing node directly to ground, causing the output (V_{OUT}) to equal the input offset multiplied by the amplifier's open-loop gain. The output thus saturates in the positive or negative direction, rendering that approach useless.

The circuit of Figure 3 overcomes this problem by ac-coupling the current transformer to the transconductance amplifier. Note that the capacitor value required depends on the application: it affects the low-frequency response, and by resonating with the transformer inductance it can produce output peaks at higher frequencies. Settling time, which depends on the values of R2 and C1, can be as much as one second. That behavior can be a problem in systems for which the amplifier is turned on for short periods only.

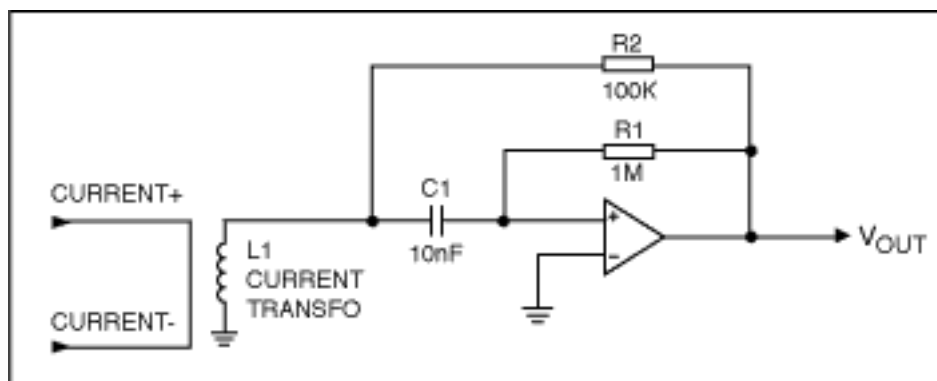


Figure 3. AC coupling enables this circuit to tolerate the zero source impedance (at dc) of a current-transformer input.

The selection of amplifier in Figure 3 depends on the application, but it generally requires wide bandwidth, low input offset, and low-power operation. A good choice for most applications is the MAX427, which specifies a 15 μ V offset and 5MHz gain-bandwidth product.

More Information

MAX427: [QuickView](#) -- [Full \(PDF\) Data Sheet](#) -- [Free Samples](#)